



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 2214

Tsuyoshi TOMITA

Art Unit: 2631

Serial No.: 09/263,766

Examiner: K. M. Burd

Filed: March 5, 1999

Docket No.: 108075-09006

For: SIGNAL PROCESSING CIRCUIT FOR PREVENTING PSEUDO LOCK OF
TIMING RECOVERY PLL

AMENDMENT UNDER 37 C.F.R. § 1.114(c)

Introductory Comments

RECEIVED

SEP 17 2004

Technology Center 2600

Director of the U.S. PTO
P.O. Box 1450
Alexandria, Virginia 22313-1450

September 16, 2004

Sir:

In response to the Office Action dated April 16, 2004, the period for response being extended from July 16, 2004 until September 16, 2004 by the attached Petition for Extension of Time, please amend the above-titled application as follows:

09/17/2004 JBALINAN 00000007 09263766

03 FC:1201

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